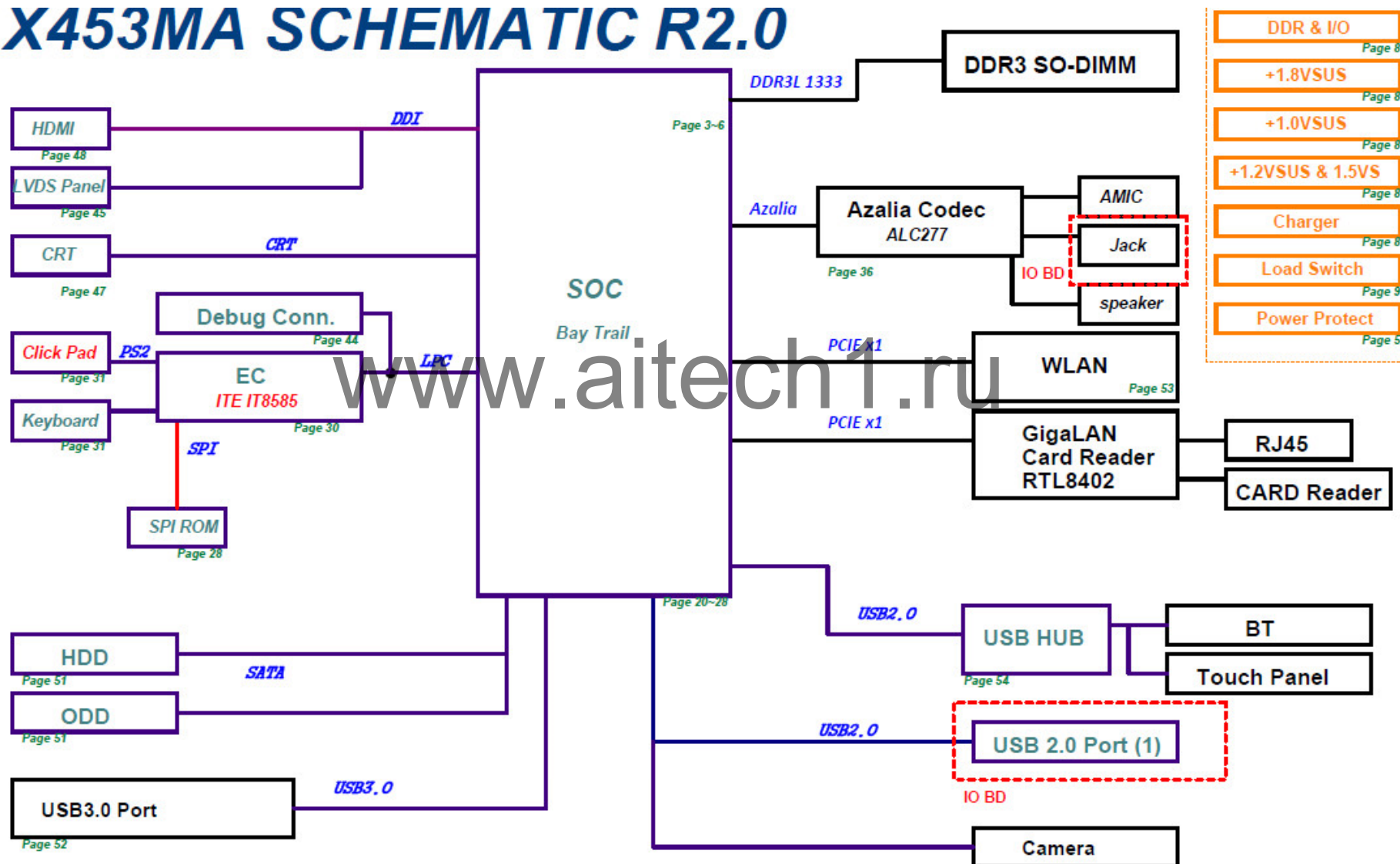
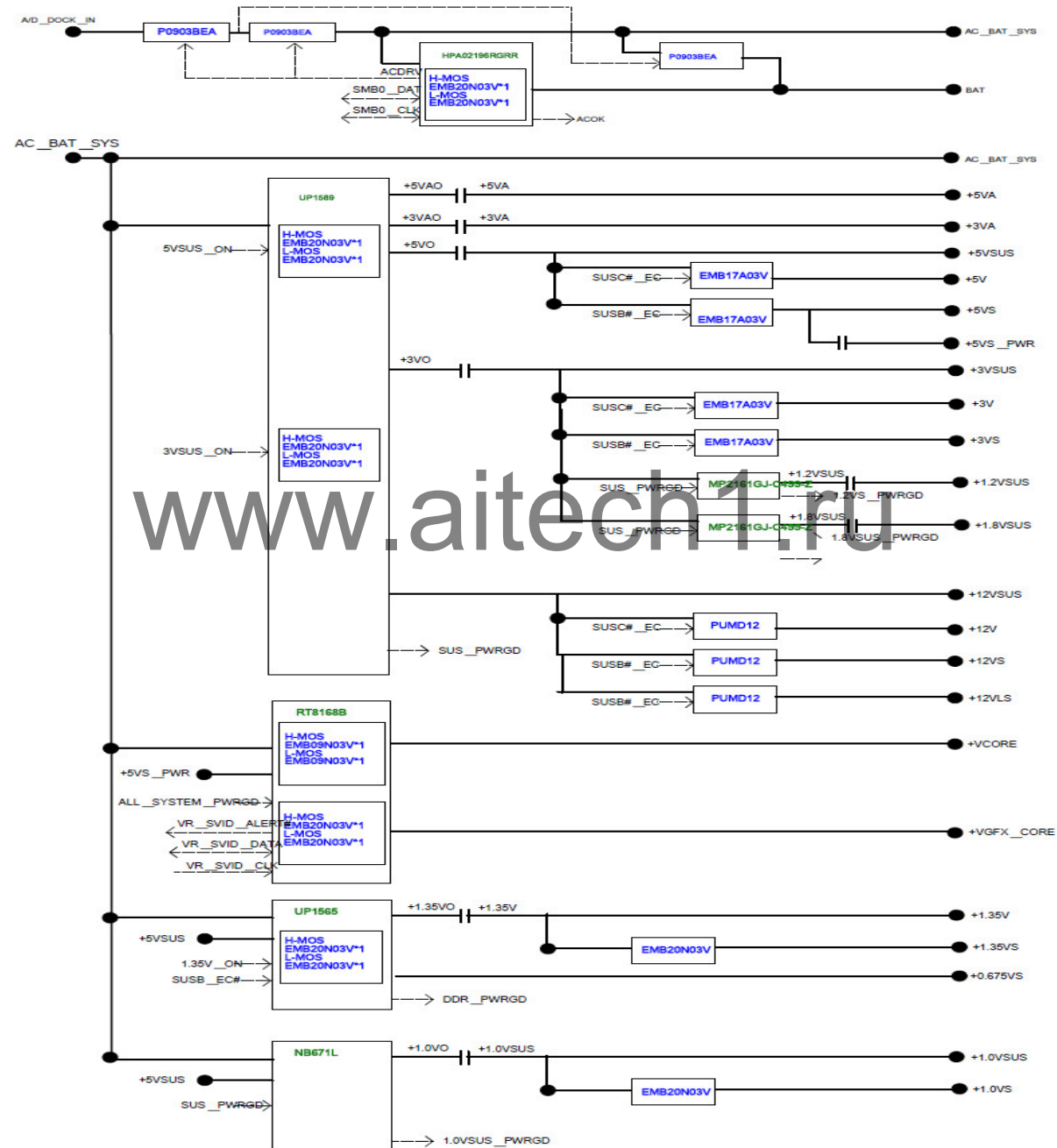


## BLOCK DIAGRAM

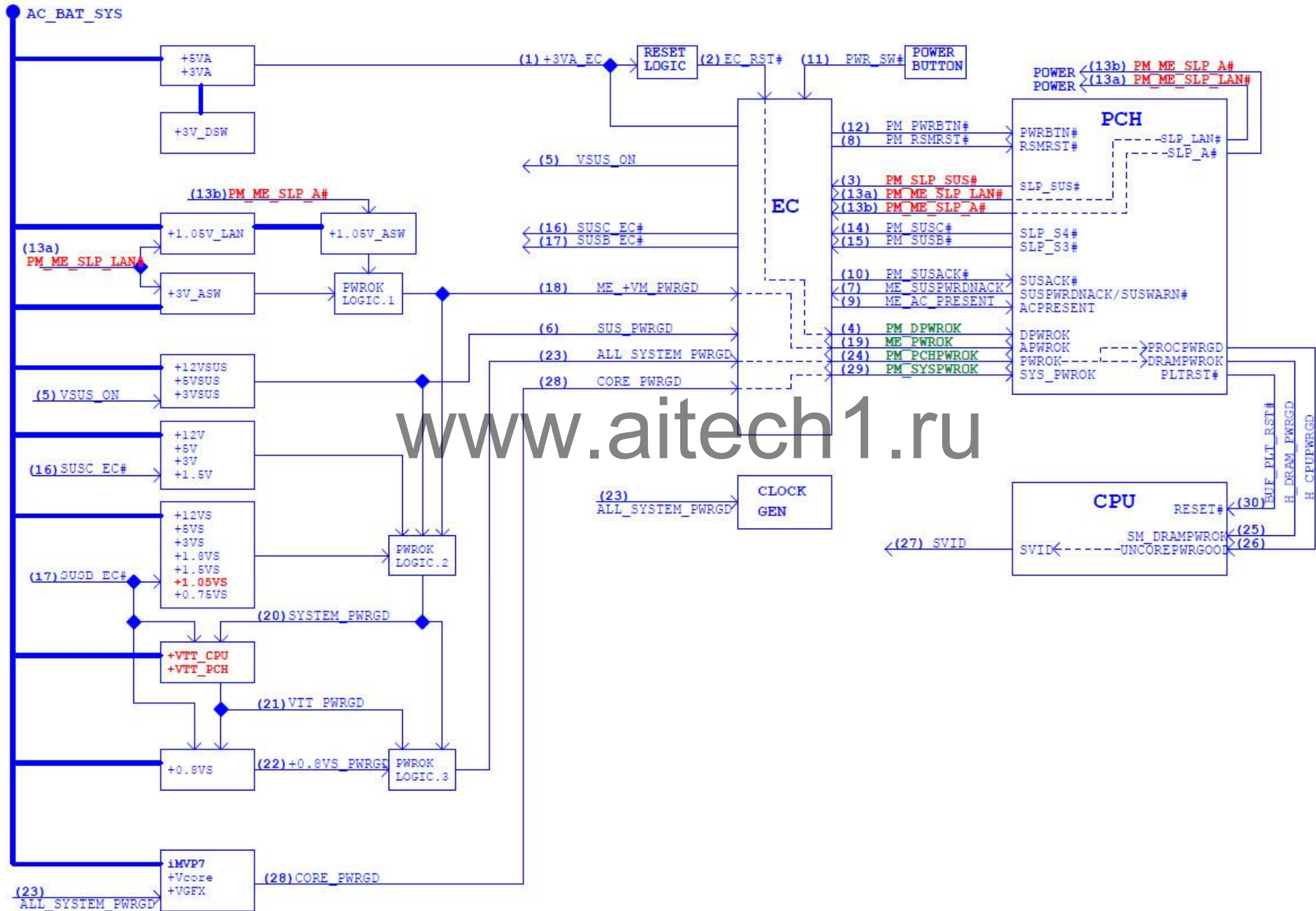
### X453MA SCHEMATIC R2.0



# POWER FLOW



# POWER ON SEQUENCE





WWW

1 Pr8806 AC\_BAT\_SYS 575

PL8301 +1.35V0 456

PL8600 +1.0VSUS0 323

26.PQ8000 VCORE 10

24.R2123 PM\_PWROK 739

25 R2124 PM\_PWROK SOC 790

14 R3041 PM\_PWRBTN# 594

16.RN3006 PM\_SUSC# 587

17. RN3006 PM\_SUSB# 736

10 Q3001 ME\_SUSPWRDNACK 672

PL8801 P\_CHG\_LX 30 532

27 C5804 CORE\_PWRGD 686

4.R3042 AC\_IN\_OC# 609

5 SL3005 +3VA\_EC 476

20 R5705 +1.8VS\_DSG 496

12 Q8002 ME\_AC\_PRESENT 601

7 PQ8108 +5VSUS\_ON 750

2 PSL8100 +5VA 552

28 Q3201 PLT\_RST# 478

6 D3204 EC\_RST 646

PL8100 +3V0 340

PL8101 +5VSUS0 504

J1601-204 +0.675VS 283

8 PQ8108 +3VSUS\_ON 747

3 PSL8101 +3VA 475

19. PQ9110 SUSB\_EC# 576

18. Q5705 SUSC\_EC # 610



## Signal Measure Point-Top

